

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L38	601	modulat\$3 same call\$3 same request\$3	USPA T; EPO; JPO	2003/03/1 0 09:03	
2	BRS	L39	70	38 and conversion same processor	USPA T; EPO; JPO	2003/03/1 0 09:00	
3	BRS	L40	28	39 and PCM	USPA T; EPO; JPO	2003/03/1 0 09:04	
4	BRS	L41	24	40 and demodulat\$3	USPA T; EPO; JPO	2003/03/1 0 09:04	
5	BRS	L42	21	41 and controller	USPA T; EPO; JPO	2003/03/1 0 09:04	
6	BRS	L45	20887	modulat\$3 same call\$3	USPA T; EPO; JPO	2003/03/1 0 09:26	
7	BRS	L46	1742	45 and PCM	USPA T; EPO; JPO	2003/03/1 0 09:04	
8	BRS	L47	795	46 and demodulat\$3	USPA T; EPO; JPO	2003/03/1 0 09:11	
9	BRS	L48	459	47 and controller	USPA T; EPO; JPO	2003/03/1 0 09:11	
10	BRS	L49	56	48 and ((attach\$3 or add\$3) same header)	USPA T; EPO; JPO	2003/03/1 0 09:12	
11	BRS	L50	51	49 and multiplex\$3	USPA T; EPO; JPO	2003/03/1 0 09:19	
12	BRS	L51	24	50 and demultiplex\$3	USPA T; EPO; JPO	2003/03/1 0 09:13	
13	BRS	L52	10	51 and ((remov\$3 or separat\$3) same header)	USPA T; EPO; JPO	2003/03/1 0 11:56	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L54	1097	45 and ((transmission or transmit\$4) same PCM)	USPA T; EPO; JPO	2003/03/10 09:26	
15	BRS	L55	515	54 and demodulat\$3	USPA T; EPO; JPO	2003/03/10 09:26	
16	BRS	L56	303	55 and controller	USPA T; EPO; JPO	2003/03/10 09:12	
17	BRS	L57	19	56 and ((attach\$3 or add\$3) with header)	USPA T; EPO; JPO	2003/03/10 09:19	
18	BRS	L58	18	57 and multiplex\$3	USPA T; EPO; JPO	2003/03/10 09:27	
19	BRS	L59	9	58 and demultiplex\$3	USPA T; EPO; JPO	2003/03/10 09:13	
20	BRS	L61	12	56 and (controller same (attach\$3 or add\$3) with header)	USPA T; EPO; JPO	2003/03/10 09:26	
21	BRS	L63	1	62 and multiplex\$3	USPA T; EPO; JPO	2003/03/10 11:55	
22	BRS	L64	5183	370/464-477.ccls.	USPA T; EPO; JPO	2003/03/10 09:25	
23	BRS	L65	146	64 and modulat\$3 with call\$3	USPA T; EPO; JPO	2003/03/10 09:26	
24	BRS	L66	60	65 and ((transmission or transmit\$4) same PCM)	USPA T; EPO; JPO	2003/03/10 09:26	
25	BRS	L67	34	66 and demodulat\$3	USPA T; EPO; JPO	2003/03/10 09:26	
26	BRS	L68	8	67 and (controller same (attach\$3 or add\$3) with header)	USPA T; EPO; JPO	2003/03/10 11:58	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
27	BRS	L70	8	68 and multiplex\$3	USPA T; EPO; JPO	2003/03/1 0 09:27	
28	BRS	L62	1	61 and ((remov\$3 or separat\$3) same header)	USPA T; EPO; JPO	2003/03/1 0 11:58	
29	BRS	L72	8	67 and (controller with (attach\$3 or add\$3) with header)	USPA T; EPO; JPO	2003/03/1 0 11:56	
30	BRS	L75	34	64 and (controller with (attach\$3 or add\$3) with header)	USPA T; EPO; JPO	2003/03/1 0 11:58	
31	BRS	L76	5	75 and (controller with (remov\$3 or separat\$3) with header)	USPA T; EPO; JPO	2003/03/1 0 12:00	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	5183	370/464-477.ccls.	USPA T; EPO; JPO	2003/03/1 0 08:11	
2	BRS	L2	170	1 and (conversion with processors)	USPA T; EPO; JPO	2003/03/1 0 08:20	
3	BRS	L3	47	2 and modulat\$3 and demodulat\$3	USPA T; EPO; JPO	2003/03/1 0 08:20	
4	BRS	L4	18	3 and controller and ((add\$3 or attach\$3) same header)	USPA T; EPO; JPO	2003/03/1 0 08:21	
5	BRS	L5	7	4 and ((remov\$3 or separat\$3) same header)	USPA T; EPO; JPO	2003/03/1 0 08:14	
6	BRS	L6	6	5 and (multiplex\$3 or demultiplex\$3)	USPA T; EPO; JPO	2003/03/1 0 08:22	
7	BRS	L7	17	1 and (plural\$4 with conversion with processors)	USPA T; EPO; JPO	2003/03/1 0 08:20	
8	BRS	L8	5	7 and modulat\$3 and demodulat\$3	USPA T; EPO; JPO	2003/03/1 0 08:21	
9	BRS	L10	6	6 and PCM	USPA T; EPO; JPO	2003/03/1 0 08:23	